

Debugging and Profiling Parallel Programs on Titan



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Outline

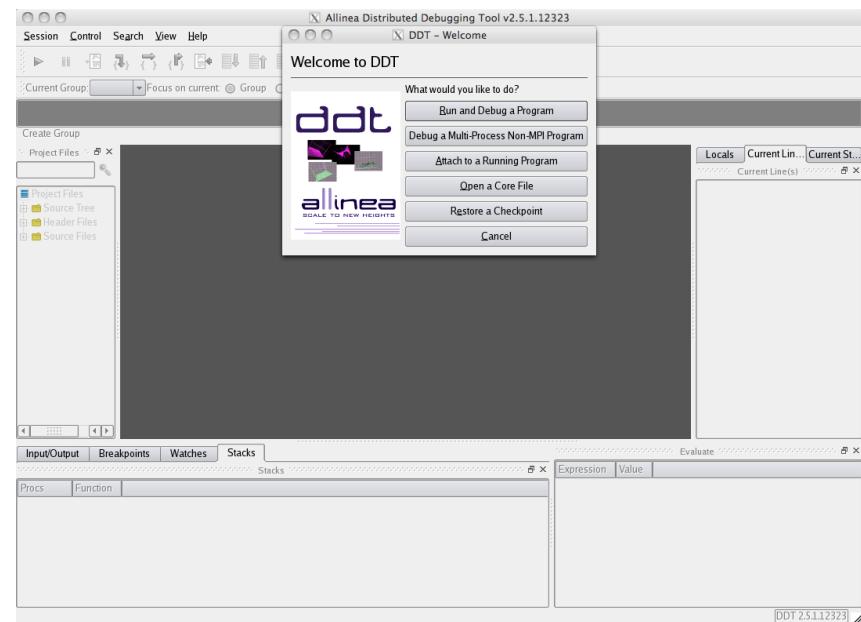
- About Allinea DDT
- DDT capabilities overview
- Using DDT
- GPU profiling

About Allinea DDT

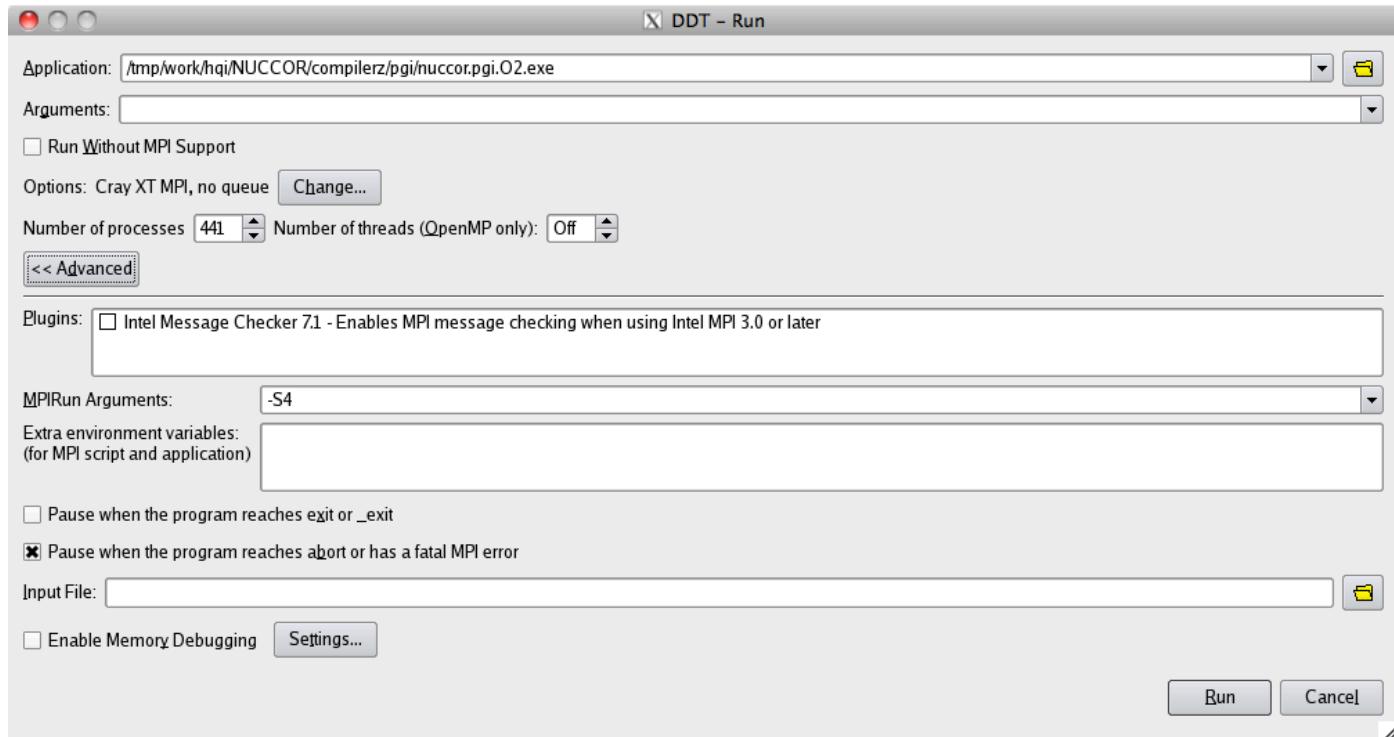
- Distributed Debugging Tool
- Capable of debugging codes written with MPI, OpenMP, threading, GPGPU
- Allinea collaborating with ORNL to create debugging tool
- Easy to use, intuitive

DDT Capabilities Overview

- Compile code with `-g` flag
- On OLCF systems:
 - `module load ddt`
 - `ddt &`
- Launch DDT from scratch directory
- Can run it within interactive job, or have DDT launch job



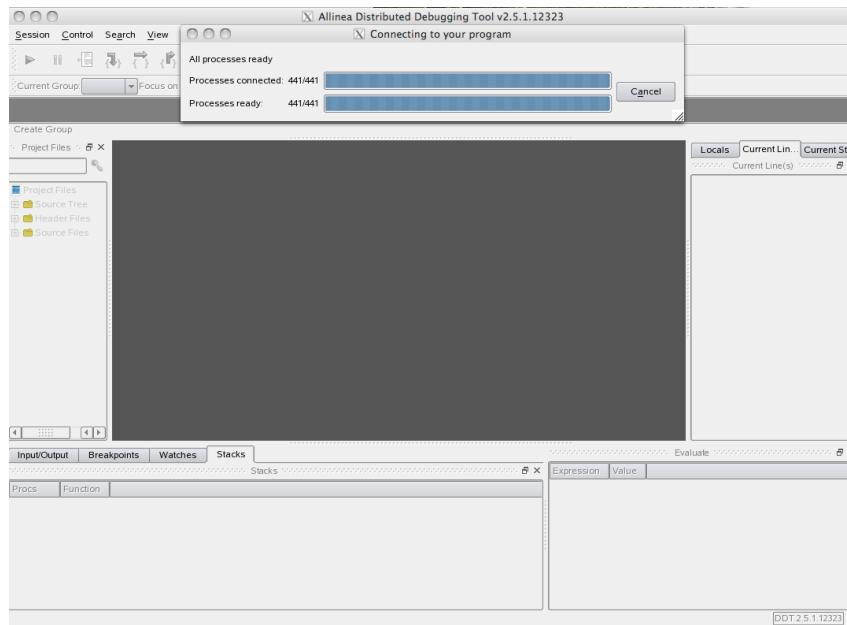
DDT Capabilities Overview



- Running a job
 - Enter application name
 - Can have DDT launch job, or run interactive job
 - Set arguments as necessary

DDT Capabilities Overview

DDT Starting up



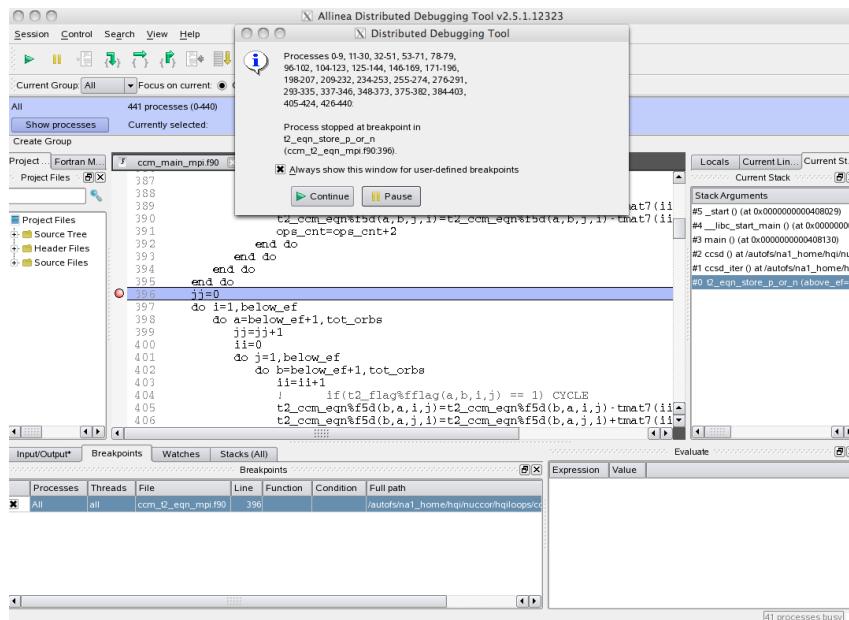
Opening Screen

This screenshot shows the DDT interface after it has connected to a program. The title bar remains the same. The main workspace now displays a code editor window for a file named "ccm_main_mpi.f90". The code contains Fortran 90 declarations and assignments. The "Stacks" tab at the bottom is also visible, showing the call stack for process 441, which includes entries for "main" and "ccsd(ccm_main_mpi.f90:40)".

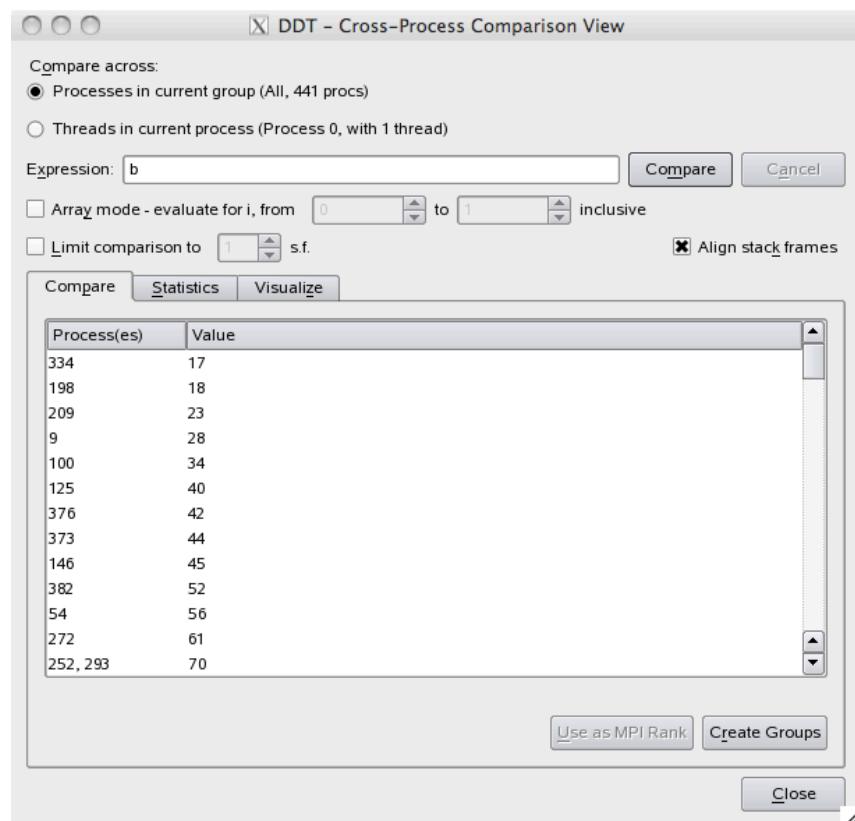
```
!-----  
95  INTEGER :: neutrot_tot_orbs,proto_tot_orbs  
96  INTEGER :: neutrot_soc,proto_soc,im_tot_orbs  
97  INTEGER :: i,j,k,maximum,big,j_a,j_b,max_big,m  
98  INTEGER :: jorb_max,iswitch,ichk2,below_sf,iwswitchbj  
99  INTEGER :: nn_upper,nn_lower,nn_upper1,nn_upper2  
100  INTEGER :: nn_lower1,nn_lower2,iph  
101  INTEGER :: a,aa,c,cc,n,ii,nn,pp,nnl  
102  REAL*8 :: re_e1, im_e1, re_hcom, im_hcom  
103  real*8, allocatable, dimension(:) :: tkin, hcom_lp  
104  CHARACTER (LEN=100) :: output1, interaction, output2, output3  
105  INTEGER :: junk1,junk2,junk3,junk4,t_z, ntot_n, ntot_p  
106  integer :: hgi, n, passing inputdata  
107  integer , dimension(4) :: tnlj  
108  real*8 , dimension(4) :: elstore  
109  
110 !-----  
111 ! Read neutron and proton single-particle data  
112 ! All of this file manipulation really needs to be done  
113 ! exclusively by P0  
114 if ( iam == master ) then  
115  
116 !-----  
117 !-----
```

DDT Capabilities Overview

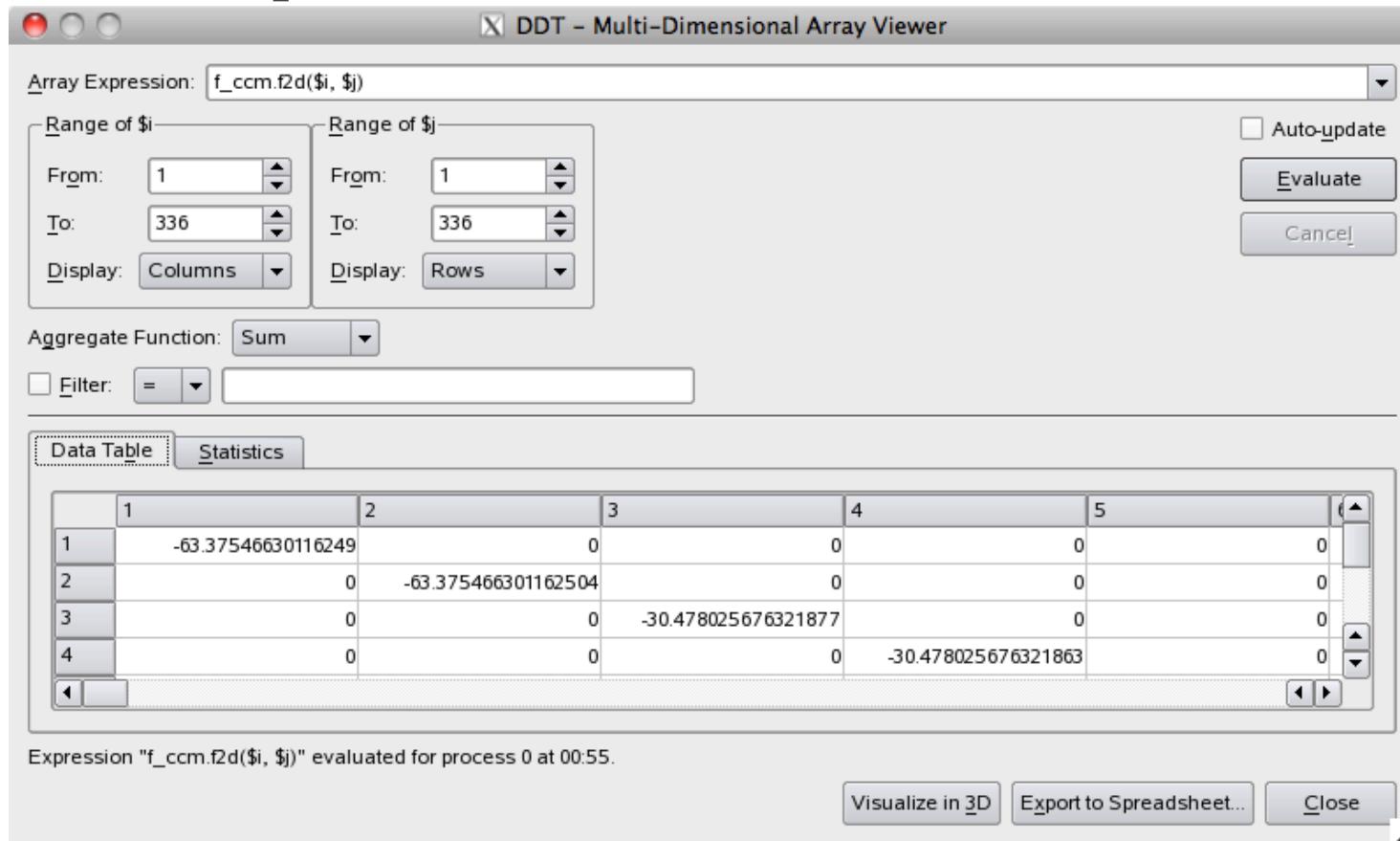
Insert Breakpoints, and Pause at that Point



Can view the value of scalar variable across all processes



DDT Capabilities Overview



- View array on single process
- Can also view statistics, visualize, evaluate subsets

Using DDT: Step 1 -- Compiling

For regular debugging:

- Compile your code with the usual compiler and `-g` flag
 - Works better if all optimizations turned off
 - For some compilers, debug flag automatically turns off optimizations
 - If optimizations are on, line numbers may be misaligned or inexact

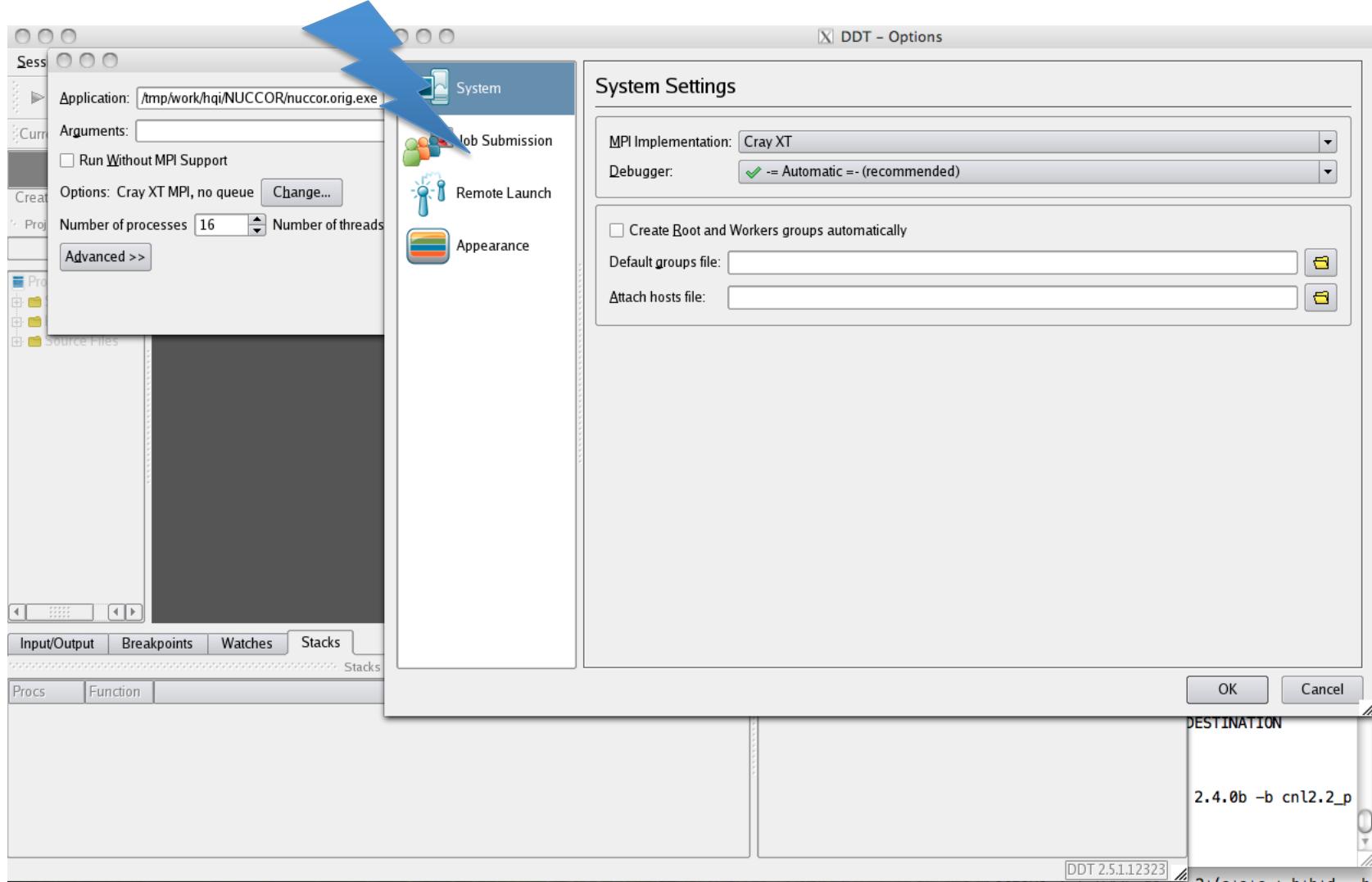
For memory debugging:

- Compile with `ddtmem*` wrappers
 - `ddtmem-ftn`
 - `ddtmem-cc`
 - `ddtmem-CC`
- Must module load `ddt` before compiling, and rename compilers in makefiles, configure, etc.

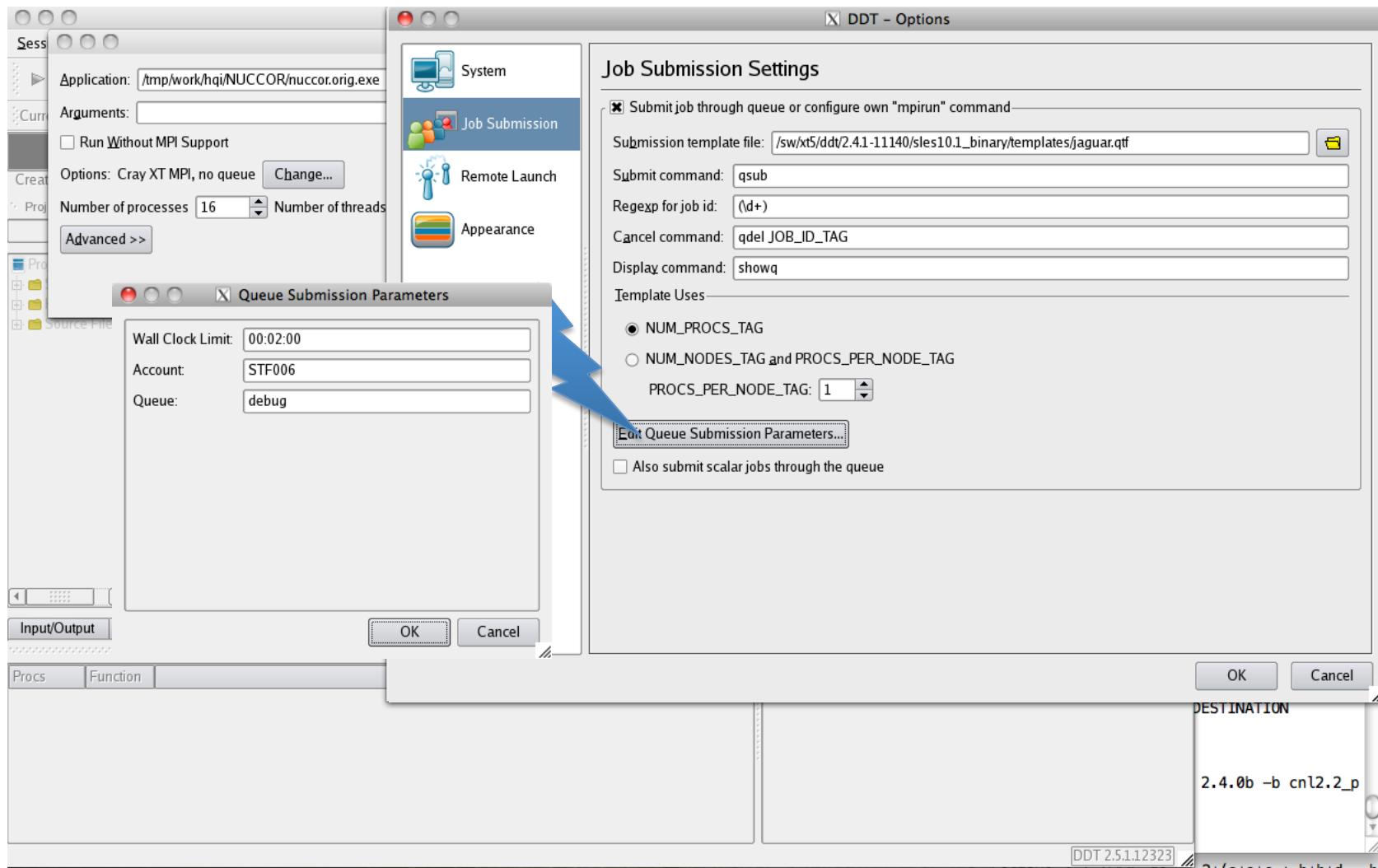
Using DDT: Step 2 -- Running

- You must have logged in with flags to allow X-forwarding:
 - ssh -X user@jaguar{pf}.ccs.ornl.gov (linux)
 - ssh -Y user@jaguar{pf}.ccs.ornl.gov (mac)
- module load ddt
- DDT can launch parallel interactive jobs for you
- Or, you can launch the interactive job and run DDT inside (I prefer this)

Setting up Queue Parameters

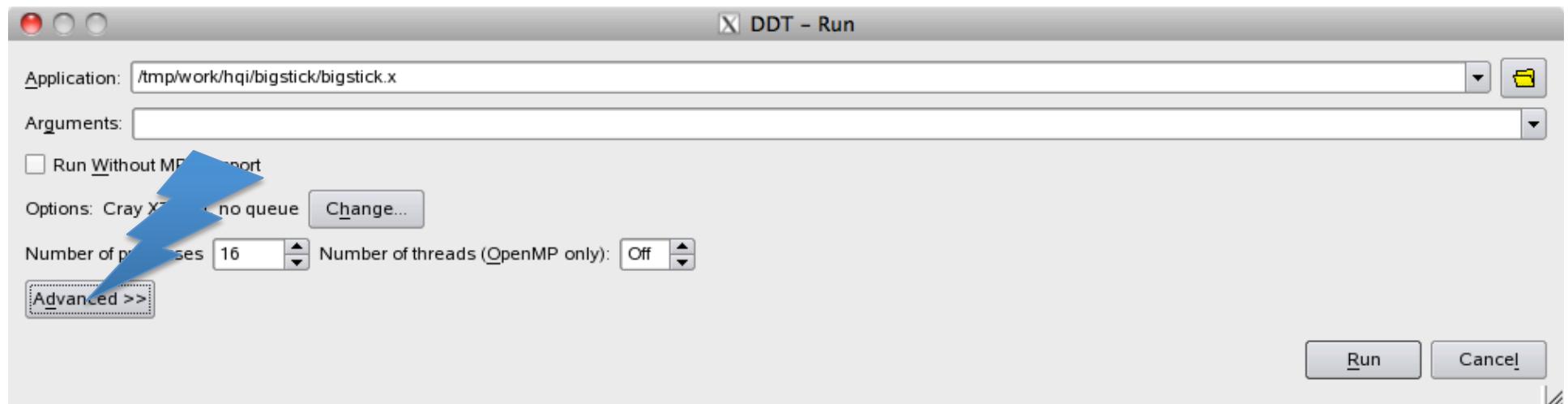


Setting up Queue Parameters



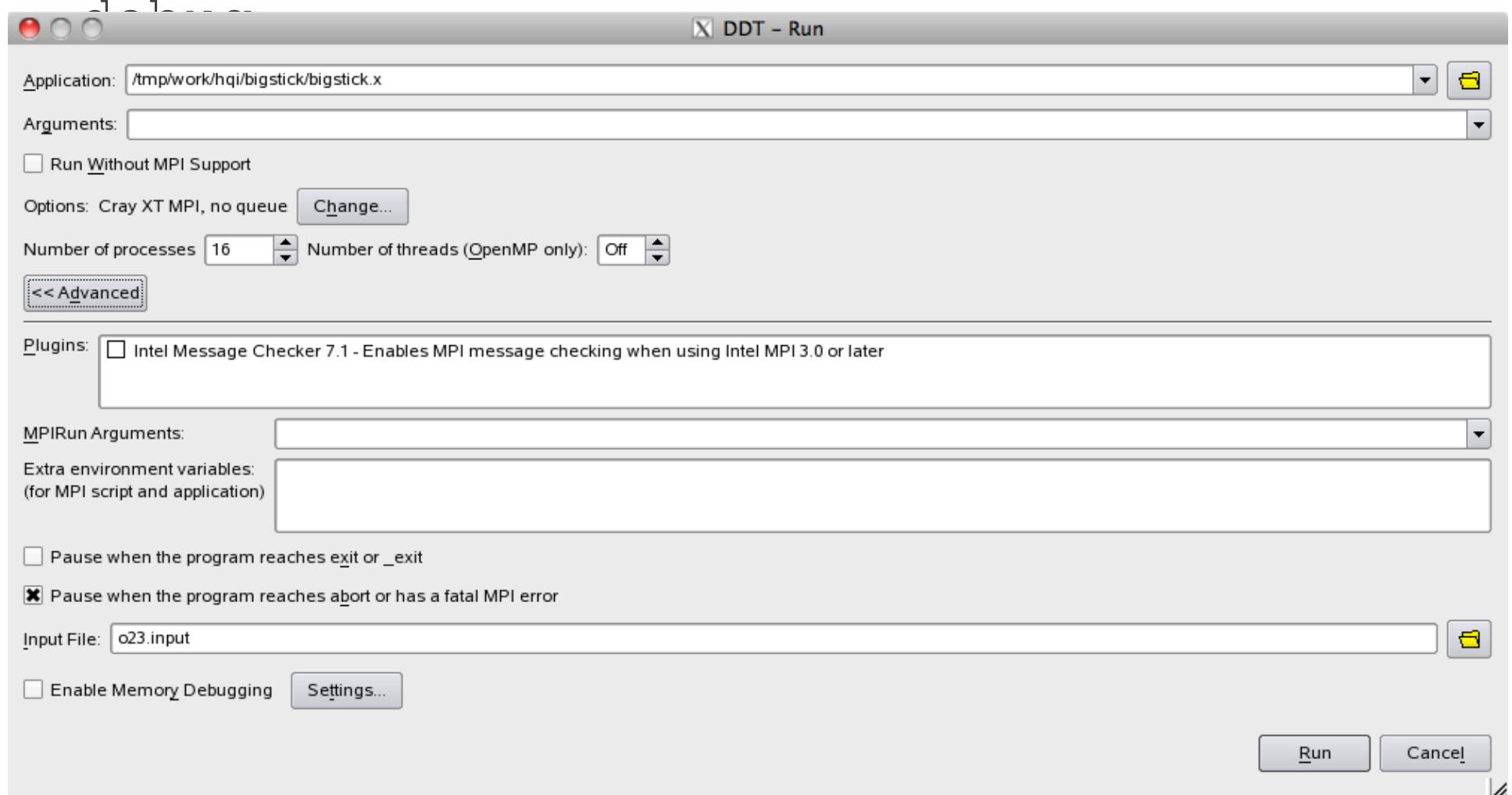
Running from Interactive Job

- qsub -I -A XYZ123 -V -lsize=12 -q debug
- Once job is running, then do ddt &



Running from Interactive Job

- qsub -I -A XYZ123 -V -lsize=12 -q



Using DDT: Step 3 -- Debugging

- Set breakpoints
- Start/Pause/restart
- Look at variables
- Look at state of program on each processor
- Run program until condition occurs (i.e., stop when $x=6$)

NVIDIA Compute Visual Profiler

The screenshot shows the NVIDIA Compute Visual Profiler application window. The title bar reads "shared - Compute Visual Profiler - [Session1 - Device_0 - Context_0 [CUDA]]". The menu bar includes File, Session, View, Options, Window, and Help. The toolbar contains various icons for file operations and analysis. The main area has two tabs: "Profiler Output" (selected) and "Summary Table". The "Summary Table" tab displays a table with the following data:

	GPU Timestamp (us)	Method	GPU Time (us)	CPU Time (us)	grid size	thread block size	static shared memory per block (bytes)	registers per thread	Occup.
7	1432	GOL	281.632	285.632	[35 74 1]	[32 16 1]	2048	14	1
8	1714	ghostRows	2.848	4	[64 1 1]	[16 1 1]	0	12	0.167
9	1720	ghostCols	4.288	8.288	[65 1 1]	[16 1 1]	0	8	0.167
10	1728	GOL	282.176	286.176	[35 74 1]	[32 16 1]	2048	14	1
11	2010	ghostRows	2.784	3	[64 1 1]	[16 1 1]	0	12	0.167
12	2016	ghostCols	4.32	7.32	[65 1 1]	[16 1 1]	0	8	0.167
13	2020	GOL	280	283	[35 74 1]	[32 16 1]	2048	14	1

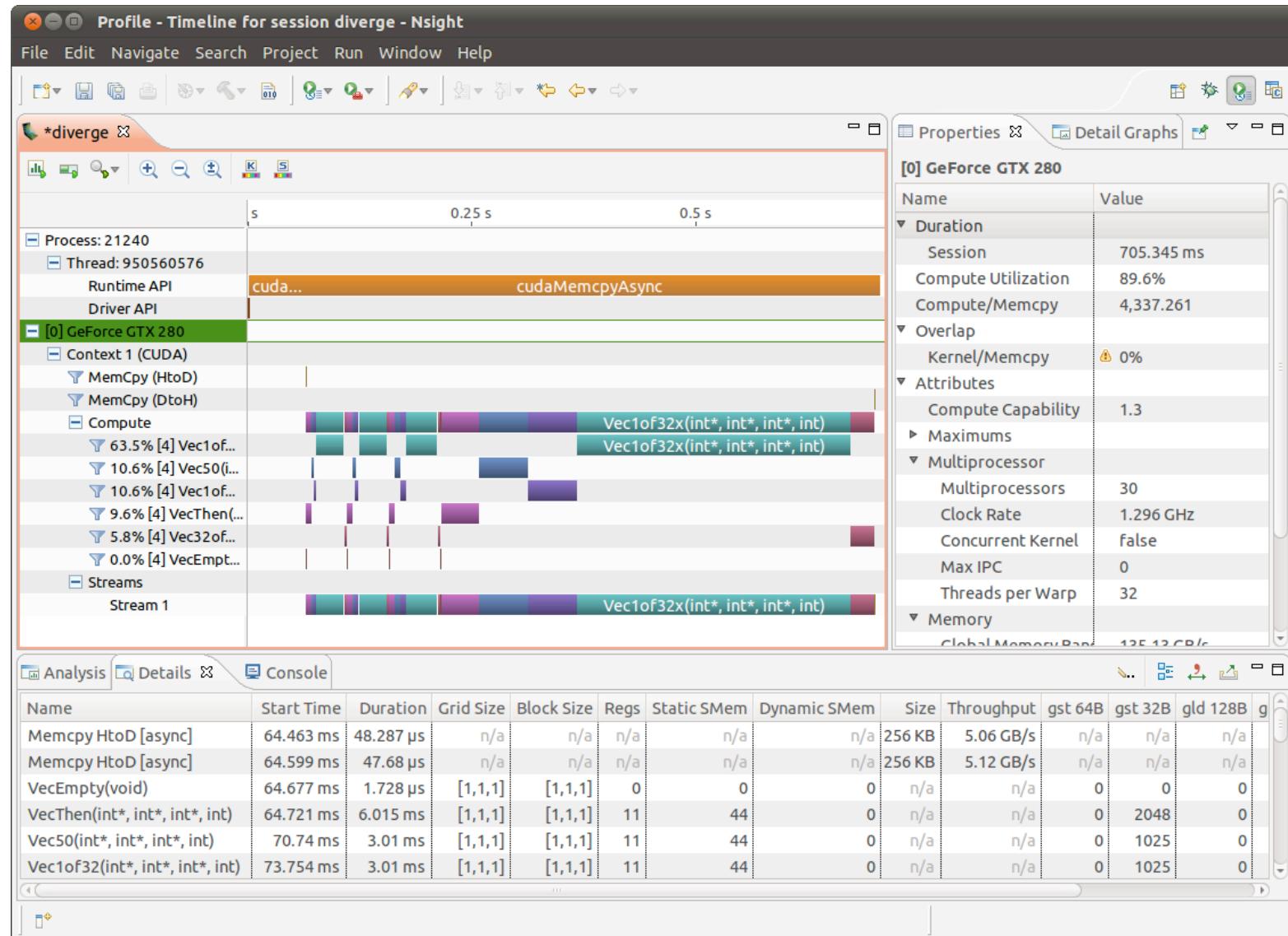
The "Analysis" pane on the left contains the following sections:

- Session1::Device_0::Context_0**
 - Kernel time = 96.90 % of total GPU time
 - Memory copy time = 0.8 % of total GPU time
 - Kernel taking maximum time = **GOL** (94.5% of total GPU time)
 - Memory copy taking maximum time = **memcpyDtoH** (0.5% of total GPU time)
 - Total overlap time in GPU = 48.8 micro sec. (0.0% of total GPU time)
- Hint(s)**
 - Double click on the kernel name in the Summary Table to analyze the kernel
 - Analyze kernel **GOL**
 - Consider using page-locked memory to attain higher bandwidth between host and device memory. Overuse of pinned memory should be avoided as it may reduce overall system performance.
Refer to the "Page-Locked Host Memory" section in the "CUDA C Runtime" chapter of the CUDA C Programming Guide for more details.

NVIDIA Compute Visual Profiler

- module load cudatoolkit
- cd /tmp/work/\$USER \$ export COMPUTE_PROFILE=1
COMPUTE_PROFILE_CSV=1
- aprun a.out
- nvvp &
- Visual IDE version: nvprof &

NVIDIA Nsight



NVIDIA Nsight integration

The screenshot shows the NVIDIA Nsight IDE interface. The main window displays the CUDA source code for `bitreverse.cu`. The code includes a host function `main` that initializes a data array and calls a CUDA kernel `bitreverse`. The CUDA kernel is defined with `__global__` and takes a pointer to an array of integers. The host function also handles memory allocation and copying between host and device memory using `cudaMalloc` and `cudaMemcpy`.

```
/*  
 * __global__ void bitreverse(void *data) {  
 *     unsigned int *idata = (unsigned int*) data;  
 *     idata[threadIdx.x] = bitreverse(idata[threadIdx.x]);  
 * }  
  
/**  
 * Host function that prepares data array and passes it to the CUDA kernel.  
 */  
int main(void) {  
    void *d = NULL;  
    int i;  
    unsigned int idata[WORK_SIZE], odata[WORK_SIZE];  
  
    for (i = 0; i < WORK_SIZE; i++)  
        idata[i] = (unsigned int) i;  
  
    CUDA_CHECK_RETURN(cudaMalloc((void**) &d, sizeof(int) * WORK_SIZE));  
    CUDA_CHECK_RETURN(  
        cudaMemcpy(d, idata, sizeof(int) * WORK_SIZE, cudaMemcpyHostToDevice));  
  
    bitreverse<<<1, WORK_SIZE, WORK_SIZE * sizeof(int)>>>(d);  
}
```

The project explorer on the left shows the `bitreverse` project with files `bitreverse.cu`, `diverge`, and `findmax`. The build console at the bottom shows the command-line output of the build process using NVCC:

```
make all  
Building file: ../src/bitreverse.cu  
Invoking: NVCC Compiler  
nvcc -G -g -O0 -gencode arch=compute_13,code=sm_13 -gencode  
arch=compute_20,code=sm_20 -gencode arch=compute_20,code=sm_21 -odir "src" -M -o  
"src/bitreverse.d" "../src/bitreverse.cu"  
nvcc --compile -G -O0 -g -gencode arch=compute_13,code=compute_13 -gencode  
arch=compute_13,code=sm_13 -gencode arch=compute_20,code=compute_20 -gencode  
arch=compute_20,code=sm_21 -o "src/bitreverse.o" "../src/bitreverse.cu"  
Finished building: ../src/bitreverse.cu
```

CrayPat

- Supports: MPI, CUDA, Cray SHMEMTM, UPC, Co-Array Fortran, OpenMP, OpenACC, C, C++ and Fortran
- module load perftools
- pat_build -O apa my_program
- Produces **my_program+pat** executable
- Submit your job with new exe
- Generate report with:
`-pat_report -T -o report1.txt my_program+pat+PID-nodesdt.xf`

Questions?

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Debugging and Profiling

Programs to aid in the correction and optimization of source code.

craypat

Cray Performance Analysis Tools (CrayPAT) can be used to evaluate program execution performance on Cray systems.

ddt

Allinea DDT is an advanced debugging tool used for scalar, multi-threaded, and large-scale parallel applications.

dyninst

Dynamic Instrumentation (Dyninst) is an API for Runtime Binary Code Manipulation.

fpmpl

FPMPI and FPMPI_papi are light-weight profiling libraries that use the pmpi hooks, as specified in the MPI standard.

gdb

The GNU Project Debugger (GDB) lets you debug programs written in Ada, C, C++, Objective-C, Pascal (and many other languages).

gptl

The General Purpose Timing Library (GPTL) is a library to instrument C, C++, and Fortran codes for performance analysis and profiling.

hpctoolkit



Hands-On Demo

- Mystery!
 - A code segfaults under certain conditions, but I don't know why
 - Can you solve the mystery?
- Instructions
 - `cp /tmp/work/hqi/mystery.tar.gz /tmp/work/username`
 - `tar -xvzf mystery.tar.gz`
 - `cd mystery; less README`
- (Alternatively, work on your own code)